	Application No.	Applicant(s)
Notice of Allowability	10/519,745	OHSHIMA, SHUNZOU
	Examiner	Art Unit
	Dharti H. Patel	2836
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to the amendment filed on 08/22/2006.		
2. The allowed claim(s) is/are <u>1-4</u> .		
<ul> <li>3.   Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a)   All   b)   Some*   c)   None   of the:  1.   Certified copies of the priority documents have been received.  2.   Certified copies of the priority documents have been received in Application No  3.   Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* Certified copies not received:</li> </ul>		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		
4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.		
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached		
1) hereto or 2) to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date		
Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).		
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5. Notice of Informal	Patent Application
Notice of References Cited (F10-032)     Notice of Draftperson's Patent Drawing Review (PTO-948)	6. Interview Summar	• •
_ , , , ,	Paper No./Mail Da	ate
<ol> <li>Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date <u>08/22/2006</u></li> </ol>	7. Examiner's Amend	imen/Comment
4. Examiner's Comment Regarding Requirement for Deposit of Biological Material		nent of Reasons for Allowance
	9.  Other	

The following is an examiner's reasons for allowance of claim 1: The prior art teaches a method of protecting a semiconductor device, comprising a DC power source, a semiconductor device, a load, a circuit element, cutting of a conduction of the semiconductor device, but does not disclose setting a constant of the circuit element so that the reference voltage is not greater than a critical voltage, wherein the critical voltage is a product of an on-resistance of the semiconductor device when its channel temperature is at an upper limit of a permissible temperature, and a minimum current value which causes the channel temperature to reach the upper limit of the permissible temperature by the self-heating due to Joule heat. This feature in combination with the rest of the claim limitations is not anticipated or rendered obvious by the prior art of record.

The following is an examiner's reasons for allowance of claim 3: The prior art teaches a protection apparatus for a semiconductor device, comprising a DC power source, a load, a semiconductor device, a circuit element, a comparator, a cut off section, but does not disclose that a constant of the circuit element is set so that the reference voltage is not greater than a critical voltage, and wherein the critical voltage is a product of the on-resistance of the semiconductor device when its channel temperature is at an upper limit of a permissible temperature, and a minimum current value which causes the channel temperature to reach the upper limit of the permissible temperature by the self-heating due to Joule heat. This feature in combination with the rest of the claim limitation is not anticipated or rendered obvious by the prior art of record.

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## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dharti H. Patel whose telephone number is 571-272-8659. The examiner can normally be reached on 8:30am - 5pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Brian Sircus can be reached on 571-272-2800, Ext. 36. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

DHP 09/08/2006

STEPHEN W. JACKSON PRIMARY EXAMINER

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